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## [DOC] Parhi Solution Unfolding

Brief Biography. Keshab K. Parhi received the B.Tech. (Honors) degree from the Indian Institute of Technology, Kharagpur (India) in 1982 in Electrical Engineering, the M.S. degree from the University of Pennsylvania in 1984 in Electrical Engineering, and the Ph.D. degree from the University of California, Berkeley in 1988 in Electrical Engineering and Computer Sciences.

## Keshab K. Parhi - Electrical and Computer Engineering

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Chap. 5 4 Properties of unfolding :  $\emptyset$ Unfolding preserves the number of delays in a DFG. This can be stated as follows:  $w/J + (w+1)/J + \dots + (w + J - 1)/J = w \emptyset$ J-unfolding of a loop l with w l delays in the original DFG leads to  $\gcd(w l, J)$  loops in the unfolded DFG, and each of these  $\gcd(w l, J)$  loops contains  $w l / \gcd(w l, J)$  delays and  $J / \gcd(w$

## Chapter 5: Unfolding

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Keshab K. Parhi. Professor of Electrical & Computer Engineering, University of Minnesota. Verified email at umn.edu - Homepage. VLSI Signal Processing VLSI Machine Learning Neuroengineering Molecular Programming. Articles Cited by Co-authors. Title. Sort. Sort by citations Sort by year Sort by title.

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